

EAST - [text-search.wsp:1]
File View Edit Tools Window Help

☐ Drafts
☐ Pending
☒ Active
☐ Failed
☐ Saved

- S1: (58388) usb
- S2: (13707) prefetch\$4 (pre-fetch\$4)
- S3: (579215) bridge
- S4: (12) S1 with S2
- S5: (2302) read with S1
- S6: (5) ahead with S5
- S7: (58) S5 with S3
- S8: (495) ata near9 S1
- S9: (8) read with S8
- S10: (4) S5 with address with (next ahead)
- S11: (2303) S1 with S3
- S12: (58) read with S11
- S13: (1975) S1 near9 (ata ide)
- S14: (55) read with S13
- S15: (82) S4 S6 S7 S9 S10 S12
- S16: (39) S14 not S15
- S17: (149) ("6618788" or ("7058748" or ("7007127" or ("6199122" or ("7010638" or ("70

☐ Favorites
☒ Tagged (71)

☒ DBs
US-PGPUB: USPAT: USOCR: EPO: JPO: DERWENT: IBM_TDB
☒ Burels
☐ Highlight all hit terms initially

Default operator: OR

BRS form
&R form
Image
Text
HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3	Im
1	<input type="checkbox"/>	US 7073010 B2	20060704	23	USB smart switch with packet re-ordering for interleaving	710/313			Chen; Ben Wei et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
2	<input type="checkbox"/>	US 6199122 B1	20010306	20	Computer system, external storage, converter system, and	710/36	709/214; 709/250;		Kobayashi; Toshiya	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
3	<input type="checkbox"/>	US 6505267 B2	20030107	16	Universal serial bus peripheral bridge with sequencer	710/315	710/105; 710/305;		Luke; David D. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
4	<input type="checkbox"/>	US 6618788 B1	20030909	21	ATA device control via a packet-based interface	710/315	710/22; 710/35		Jacobs; Daniel G.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
5	<input type="checkbox"/>	US 5828854 A	19981027	14	Method and apparatus for multiplexing signals from a bus	710/315			Wade; Nicholas D.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
6	<input type="checkbox"/>	US 7010638 B2	20060307	16	High speed bridge controller adaptable to non-standard	710/306	710/315; 710/52		Deng; Brian Tse et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US
7	<input type="checkbox"/>	US 7054980 B2	20060530	10	Multiple drive controller	710/305	710/8; 711/114		Wurzburg; Henry	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US

☒ Hits
☐ Details
☒ HTML

Ready
NUM

710/313
315
710/62
712/207

710/ 205-315
710/62-66
712/207

EAST - [Untitled1:1]

File View Edit Tools Window Help

☐ Drafts
☐ Pending
☐ Active
☐ Failed
☐ Saved
 S1: (296) pld near9 flash
 S2: (4) clock with S1
 S3: (568) pld near9 clock
 S4: (88) configur\$5 with 33
☐ Favorites
 Tagged (9)
 UDC
 Queue
 Trash

DBs: US-PG-PUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM; TDB

Default operator:

configur\$5 with S3

☒ Plurals
☐ Highlight all hit terms initially

☒ BRS form ☒ S&R form ☐ Image ☐ Text ☐ HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	
2	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6889368 B1	20050503	12	Method and apparatus for local	716/4	716/16;		Mark; David et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6810514 B1	20041026	12	Controller arrangement for parti	716/16	716/17		Alfke; Peter H. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6748456 B1	20040608	17	PLD configuration port architect	710/1	326/39;		Stanton; Richard L. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6618686 B2	20030909	14	System and method for testing	702/120			Allamsetty; Chakravarth	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6553523 B1	20030422	9	System and method for verifyin	714/725	326/38;		Lindholm; Jeffrey V. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6507425 B1	20030114	7	Infrared communication system	713/500			Dewberry; Brandon Scott	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6487648 B1	20021126	20	SDRAM controller implemented	711/167	326/39;		Hassoun; Joseph H.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	<input type="checkbox"/>	<input checked="" type="checkbox"/> US 6425077 B1	20020723	12	System and method for reading	713/1	710/8;		Le; Thach-Kinh et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	<input type="checkbox"/>	<input type="checkbox"/> US 20060097901	20060511	21	Analog-to-digital converter ope	341/155			Draxelmayr; Dieter et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	<input type="checkbox"/>	<input type="checkbox"/> US 20060038585	20060223	15	Apparatus and methods for co	326/38			Krishnappa; Mukunda et	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	<input type="checkbox"/>	<input type="checkbox"/> US 20060030914	20060209	95	System, method and apparatus	607/103	600/546;		Eggers; Philip E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
13	<input type="checkbox"/>	<input type="checkbox"/> US 20060030913	20060209	97	System, method and apparatus	607/103	600/546;		Eggers; Philip E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
14	<input type="checkbox"/>	<input type="checkbox"/> US 20060030912	20060209	97	System, method and apparatus	607/103	600/549;		Eggers; Philip E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
15	<input type="checkbox"/>	<input type="checkbox"/> US 20060020313	20060126	104	System, method and apparatus	607/103	607/113		Eggers; Philip E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
16	<input type="checkbox"/>	<input type="checkbox"/> US 20060020312	20060126	97	System, method and apparatus	607/103	607/113		Eggers; Philip E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
17	<input type="checkbox"/>	<input type="checkbox"/> US 20060005090	20060105	29	Compare, select, sort, and med	714/725			Langhammer; Martin et	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
18	<input type="checkbox"/>	<input type="checkbox"/> US 20050242867	20051103	49	Differential clocking scheme in	327/291			Ghia; Atul V. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
19	<input type="checkbox"/>	<input type="checkbox"/> US 20050242866	20051103	50	Programmable logic device ha	327/291			Vadi; Vasisht Mantra et	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
20	<input type="checkbox"/>	<input type="checkbox"/> US 20050001652	20050106	7	Programmable logic device co	326/38	710/8		Malik; Vipin et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
21	<input type="checkbox"/>	<input type="checkbox"/> US 20040252705	20041216	11	Flexible media access control ar	370/401	370/420;		Zancan; Mario et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
22	<input type="checkbox"/>	<input type="checkbox"/> US 20040183564	20040923	22	Programmable logic devices wit	326/3			Schadt; John A. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
23	<input type="checkbox"/>	<input type="checkbox"/> US 20040155684	20040812	25	Multi-purpose digital frequency	327/115	713/500		Nguyen; Andy T.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
24	<input type="checkbox"/>	<input type="checkbox"/> US 20040122494	20040624	104	System, method and apparatus	607/103			Eggers; Philip E. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>